

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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PATENT NUMBER

U.S. **UTILITY** Patent Application

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| <p><i>gh</i> O.I.P.E.</p> <p>SCANNED <i>1750</i> Q.A. <i>AA</i></p> | PATENT DATE. |
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| APPLICATION NO. 09/624321 | CONT/PRIOR | CLASS 438 | SUBCLASS 618 | ART UNIT 2812 2812 | EXAMINER J. W. H. |
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NTS Bing-Chang Wu

APPLICANTS

TITLE

Method and structure for reducing capacitance between interconnect lines

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| | <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | | | |
| ISSUE FEE | | | | |
| | _____ (Primary Examiner) (Date) | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |
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(FACE)